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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): M. FUNABASHI

Serial No.: 09/902,673

Filed: July 12, 2001

For: METHOD OF MANUFACTURING A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group: 2825

Examiner: L. Malsawma

PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents
Washington, D.C. 20231

May 7, 2002

Sir:

In the matter of the above-identified application, applicants hereby respectfully petition for an extension of time to permit filing a response within the second month subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed December 7, 2001. A Credit Card Payment Form in the amount of \$400.00 to cover the required fee for the requested extension of time is attached hereto.

05/08/2002 HGEEDREN1 00000085 09902673

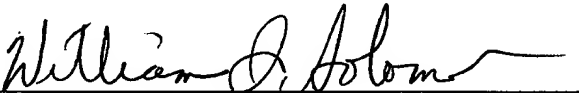
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It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 843.37558VX1).

Respectfully submitted,

ANTONELLI, TERRY STOUT & KRAUS, LLP



William I. Solomon
Registration No. 28,565

WIS/slk
Enclosures